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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/580,466	05/25/2006	Hiromi Kawamura	2006_0799A	4355
52349	7590	08/03/2007	EXAMINER	
WENDEROTH, LIND & PONACK L.L.P. 2033 K. STREET, NW SUITE 800 WASHINGTON, DC 20006			VO, TUYEN KIM	
ART UNIT	PAPER NUMBER		2876	
MAIL DATE	DELIVERY MODE		08/03/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/580,466	KAWAMURA ET AL.	
	Examiner Tuyen Kim Vo	Art Unit 2876	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 2 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on _____.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) _____ is/are rejected.
- 7) Claim(s) 1-12 is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 25 May 2006 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date: _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>5/25/2006</u> . | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Remarks

1. This application is in condition for allowance except for the following formal matters:

The drawing and claim objections are needed to be corrected as specified below.

Drawings

2. Figures 1-3 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Objections

3. Claims 1 and 9-12 are objected to because of the following informalities:

Re claim 1, the recitation of "capable of executing" in line 1 is suggested to be rephrased since it is not a positive limitation. Similar problem for the same limitation as recited in claims 9-12. Appropriate correction is required.

Allowable Subject Matter

4. Claims 1-12 would be allowable if rewritten or amended to overcome the objection(s) set forth in this Office action.
5. The following is an examiner's statement of reasons for allowance: The prior art of record, taken alone or in combination, fails to teach or fairly suggest the arrangement of a semiconductor memory which is capable of executing at least two application programs that comprising a first storage unit operable to store data relating to a first application program; a second storage unit operable to store data relating to a second application program; and especially, a level identification unit operable to identify respective security levels of the first and second application programs for the data relating to the first and second application programs, based on a criterion for identifying a security level of an application program for data relating to the application program; and a duplication unit operable to duplicate the data stored in the first storage unit and to store the duplicated data into the second storage unit, without taking the data outside of the semiconductor memory, in the case where a relationship between the two security levels identified by the level identification unit meets a predetermined condition as recited in claims 1 (and its dependents) and 9-12.

References Hosogoe et al. (US 6,766,961 B2), Taylor (US 5,578,808), Gochi (5,055,661) teach an integrated circuit (IC) card that having a plurality of memory units operable to store different application data.

References Johnson et al. (US 5,813,009), Bradley et al. (US 6,957,256 B1) and Yakir et al. (US 2004/0049513 A1) teach a data management system with security level access.

However, without the benefit of applicant's teaching, there is no motivation for one of ordinary skill in the art at the time the invention was made to selectively combine the features of the prior art in a manner so as to create the claimed invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Prosecution on the merits is closed in accordance with the practice under *Ex parte Quayle*, 25 USPQ 74, 453 O.G. 213, (Comm'r Pat. 1935).

A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Hosogoe et al. (US 6,766,961 B1), Taylor (US 5,578,808), Gochi (5,055,661), Johnson et al. (US 5,813,009), Bradley et al. (US 6,957,256 B1) and Yakir et al. (US 2004/0049513 A1) have been discussed above.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuyen Kim Vo whose telephone number is 571-270-

Art Unit: 2876

1657. The examiner can normally be reached on Monday - Friday, 7:30a.m. - 5:00p.m., EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael G. Lee can be reached on 571-272-2398. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

TKV
Tuyen Kim Vo
Patent Examiner
Art Unit 2876
July 23, 2007.

MGL
MICHAEL G. LEE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800